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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				Application Number	10/796,520
				Filing Date	March 8, 2004
				First Named Inventor	Shubhendu S. Mukherjee
				Art Unit	Unknown
				Examiner Name	Unknown
Sheet	2	of	2	Attorney Docket Number	42P18930

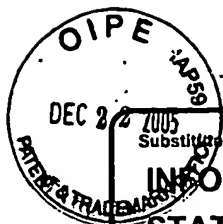
NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
CN	3	SHIBAYAMA, SHIGEKI, et al., "An Optical Bus Computer Cluster with a Deferred Cache Coherence Protocol," Proceedings of the International Conference on Parallel and Distributed Systems, IEEE Computer Society Inc., pp. 175-182, June 3, 1996		
CN	4	Invitation to Pay Additional Fees and Annex, 3 pgs., (PCT/US2005/007087), date of mailing August 15, 2005		

Examiner Signature	/Camvan Nguyen/	Date Considered	06/07/2006
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Substitution for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)				<b>Complete if Known</b>	
Application Number		10/796,520			
Filing Date		March 8, 2004			
First Named Inventor		Shubhendu S. Mukherjee			
Art Unit		2188			
Examiner Name		Padmanabhan, Mano			
Attorney Docket Number		42P18930			
Sheet	1	of	2		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.†	Document Number	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code* (if known)			
CN	1	US-2002/124144 A1	09-05-2002	GHARACHORLOO et al.	
CN	2	US-5,781,733	07-14-1998	STILES	
CN	3	US-6,122,715	09-19-2000	PALANCA et al.	
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